

**REMARKS**

By this amendment, claims 8, 16, 19-20 have been amended. Claims 11-12 and 15 have been canceled. Claims 1-10, 13-14, and 16-23 are pending in the application. Applicants reserve the right to pursue the original claims and other claims in this and other applications.

Applicants respectfully direct the Examiner's attention to the fact that the Attorney Docket Number was changed to M4065.0826/P826 along with the Revocation of Power of Attorney and New Power of Attorney submitted on February 13, 2003. It is requested that all future correspondence contain this new Attorney Docket Number.

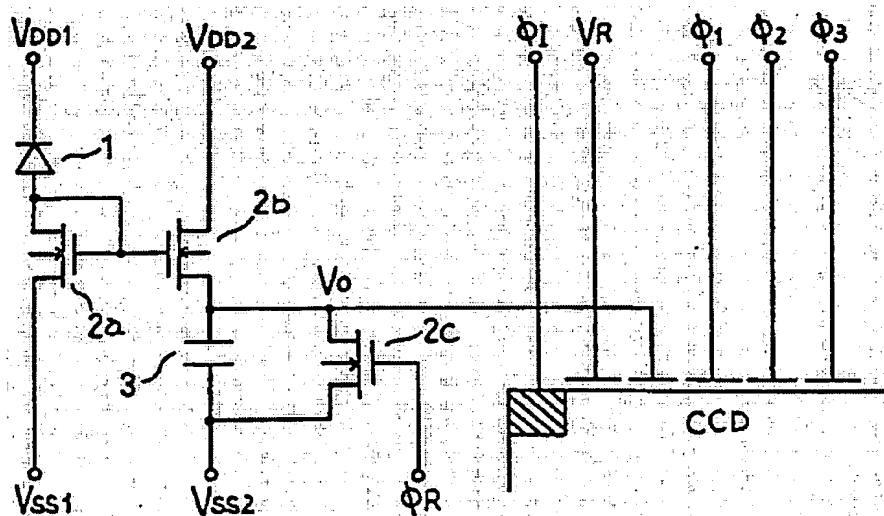
Claims 8 and 19 stand objected to for informalities and have been amended as suggested in the Office Action. Claim 16 has been amended as well to correct the same typographical error. Accordingly, the claims are in condition for allowance.

Claims 20-21 stand rejected under 35 U.S.C. § 112, first paragraph, as failing to comply with the written description requirement. Claim 20 has been amended to clarify the limitation. Applicant respectfully requests that the rejection of these claims be withdrawn.

Claims 8-10, 16-17, and 19 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Dierickx et al. (EP 0858212 A1) in view of Miyatake et al. (US 5,241,575). This rejection is respectfully traversed. In order to establish a *prima facie* case of obviousness "the prior art reference (or references when combined) must teach or suggest all the claim limitations." M.P.E.P. §2142. Neither Dierickx et al. nor Miyatake et al., even when considered in combination, teach or suggest all limitations of independent claims 8, 16, and 19.

Claims 8 and 19 recite, *inter alia*, “a first switch ...; a second switch ...; and a third switch for connecting the analog memory to a reset voltage source.” (Emphasis added.) Claim 16, as amended, recites, *inter alia*, a method of operating a CMOS image sensor comprising “resetting an analog memory of the image sensor by activating a first switch and connecting the analog memory to a reset voltage source.” Miyatake et al. does not teach or suggest this limitation. Miyatake et al. teaches shorting a capacitor 3 by electrically connecting its terminals to each other when transistor 2C is activated. Fig. 3A (reproduced below). There is no third switch for connecting the analog memory to a reset voltage source. Nor is Dierickx et al. cited for this limitation. Thus, Dierickx et al. does not remedy the deficiency of Miyatake et al. Since Miyatake et al. and Dierickx et al. do not teach or suggest all of the limitations of claims 8, 16, and 19, claims 8, 16, and 19 are not obvious over the cited references. Claims 9-10 depend from claim 8 and are patentable at least for the reasons mentioned above. Claim 17 depends from claim 16 and is patentable at least for the reasons mentioned above. Applicant respectfully requests that the 35 U.S.C. § 103(a) rejection of claims 8-10, 16-17, and 19 be withdrawn.

Miyatake et al. Fig. 3A



Claim 18 stands rejected under 35 U.S.C. § 103(a) as being unpatentable over Dierickx et al. in view of Miyatake et al., and further in view of Fossum et al. (US 5,471,515). This rejection is respectfully traversed. Claim 18 depends from claim 16 and is patentable at least for the reasons mentioned above. Applicant respectfully requests that the 35 U.S.C. § 103(a) rejection of claim 18 be withdrawn.

Claim 22 stands rejected under 35 U.S.C. § 103(a) as being unpatentable over Dierickx et al. in view of Miyatake et al., and further in view of Gowda et al. (US 6,344,877). This rejection is respectfully traversed. Claim 22 depends from claim 19 and is patentable at least for the reasons mentioned above. Applicant respectfully requests that the 35 U.S.C. § 103(a) rejection of claim 22 be withdrawn.

The statement that claims 1-7 are allowable is gratefully acknowledged. Claims 13-14 and 23 stand objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claims and any intervening claims. Claims 13-14 and 23 depend, respectively, directly, or indirectly from independent claims 8 and 19, and are allowable for at least the reasons set forth above.

In view of the above, each of the presently pending claims in this application is believed to be in immediate condition for allowance. Accordingly, the Examiner is respectfully requested to withdraw the outstanding rejection of the claims and to pass this application to issue.

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Respectfully submitted,

By 

Thomas J. D'Amico

Registration No.: 28,371

Rachael Lea Leventhal

Registration No.: 54, 266

DICKSTEIN SHAPIRO MORIN &

OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

Attorneys for Applicant